

FIG. 1 is a block diagram of a system for processing an input image.

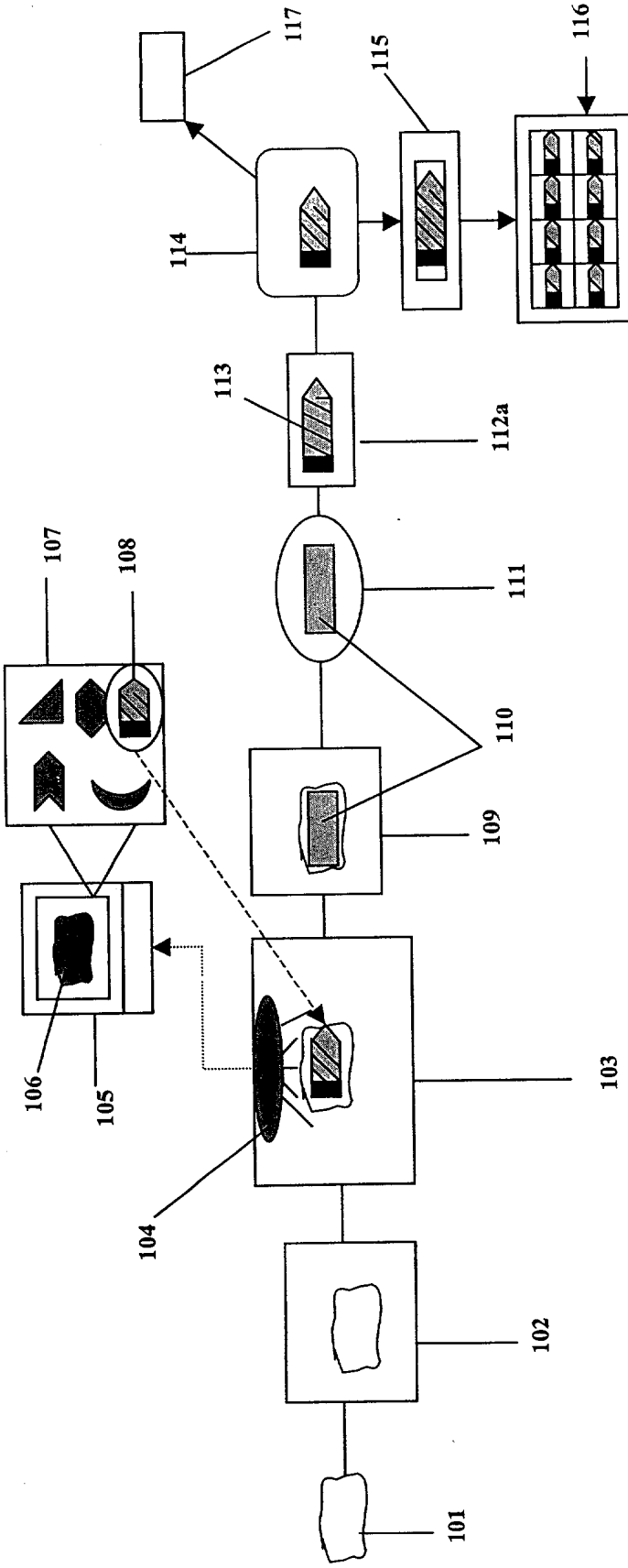


Figure 1

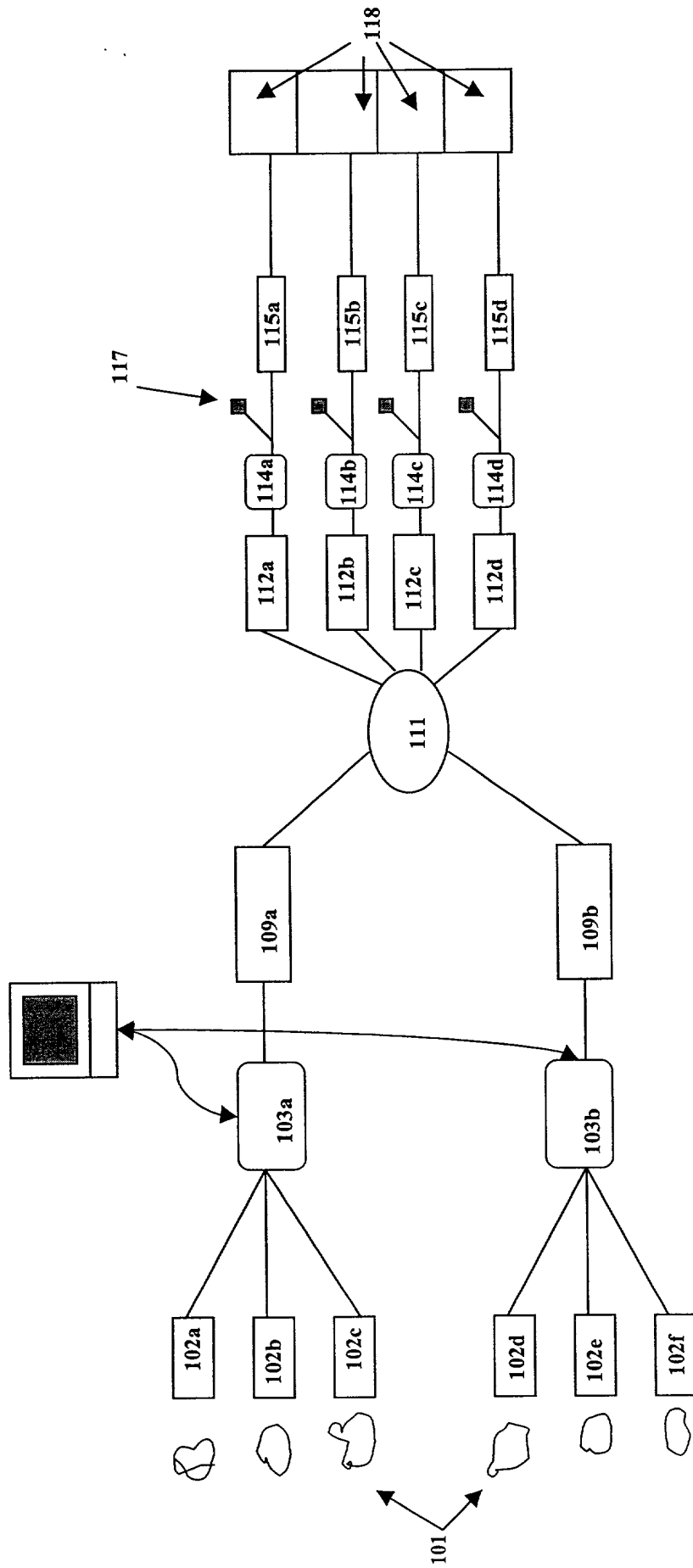


Figure 2